

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Vernon M. Williams

Serial No.: 10/619,963

Filed: July 15, 2003

For: STEREOLITHOGRAPHIC METHODS FOR SECURING CONDUCTIVE ELEMENTS TO CONTACTS OF SEMICONDUCTOR

DEVICE COMPONENTS

Confirmation No.: 7786

Examiner: P. Dang

Group Art Unit: 3641

Attorney Docket No.: 2269-4208.8US

(99-0316.08/US)

Notice of Allowance Mailed:

February 2, 2004

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EV 325782156 US

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AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 15 of this paper.

Amendments to the Drawings appear on page 18 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks begin on page 19 of this paper.

An **Appendix** including amended drawing figures is attached following page 19 of this paper.